

Atomically Precise Manufacturing

Long-term Vision

- Take dimensional precision to its limit: the crystal structure of the material.
- Top Down Control to create designed 3D structures, atom by atom.
- Atomic Precision (± 1 atom size) at first, moving to Absolute Precision in the future.

Atomically-Precise Patterning

- Atomically-precise patterns are drawn on Si(001):H using STM-based H depassivation lithography.
- We define patterns with respect to a pixel comprising 2 Si dimers on the same dimer row, giving a pixel size of 0.768 nm \times 0.768 nm.
- With this pixel basis, we can define and draw arbitrary structures at the atomic scale



Atomically Precise Pattern



Pattern Transfer by Selective Deposition

How to transfer these atomically-precise patterns into 2D or 3D sructures?



Many attempts at metal or molecular selective adsorption.

Selective Deposition in STM Lithography-defined Patterns: transfer into 2D and 3D patterns

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Dopant Patterning for 2D devices





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PH₃ selectively adsorbed into two patterns on Si(001):H

- Atomically-precise placement of P atoms deposited from PH₃, has been used to make P-based spin or charge qubit devices.
- We aim to develop similar processes for acceptor dopant precursors, such as diborane, or alanes.

Patterned Atomic Layer Epitaxy (P-ALE+) of Si

- During growth, the surface temperature must be less than 300°C, the onset of H atom mobility.
- Pattern the growth area.
- Move tip away to avoid shadowing of disilane.
- Expose to a saturation dose of disilane, which fills the area with SiH_x fragments, while being unreactive with the background H-terminated dimers.
- Repeat HDL to remove H from the disilane fragments, leaving the Si atoms behind, which form about ¹/₃ ML of islands.
- After 3 cycles of disilane exposure and tip-driven H removal, a monolayer of Si will have been deposited.
- Growth of several ML has been performed, but the surface becomes progressively rougher.



Owen et al., J. Vac. Sci. Technol. B 29 06F201 (2011) **DOI**: 10.1116/1.3628673

Patterned Atomic Layer Deposition of TiO₂

- Pattern the growth area using HDL
- Move surface to ALD chamber.
- Grow using standard ALD process, with TiCl₄ and water as precursors.
- Etch using Reactive Ion Etching (RIE)
- Master can be used to transfer pattern into resist using NanoImprint Lithography.
- Pattern can thereby be transferred into any material.

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Defect Nucleation sites for Background Growth



While there is strong chemical contrast between the patterns and the ideal Hterminated surface, the passivity is limited in practice by missing H atoms, which provide nucleation sites for background growth.

- in ambient conditions.
- Development of a method to transfer the H resist into a more robust material would be highly beneficial for this technique
- Development of a method to pattern a noble metal such as Su, Pt etc. would open a wide range of possible applications.
- Encyclopedia of Interfacial Chemistry: Surface Science and Electrochemistry article in proof stage: DOI: 10.1016/B978-0-12-409547-2.13149-X

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